



Docket No.: M4065.0646/P646  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent Application of:  
Howard E. Rhodes

Application No.: 10/611,892

Confirmation No.: 3670

Filed: July 3, 2003

Art Unit: 2815

For: OPTIMIZED TRANSISTOR FOR IMAGER  
DEVICE

Examiner: M. C. Landau

**SUBMISSION OF REPLACEMENT DRAWINGS**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated April 23, 2007, indicating an inconsistency in the drawings, please amend the above-identified U.S. patent application as follows:

**Amendments to the Drawings** are discussed in the remarks, which begin on page 2 of this paper.